

Notice of References Cited	Application/Control No. 10/706,931		Applicant(s)/Patent Under Reexamination NAKATA, KENICHI	
	Examiner Harry Behm		Art Unit 2838	Page 1 of 1

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*	B	US-2002/0135345	09-2002	Terashi, Hiroto	323/283
*	C	US-6,865,682	03-2005	Talbot et al.	713/300
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Talbot, Gerald; Microprocessor module with integrated voltage regulators; December 28, 2000; WO 2000/79370.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.